

Ted Johansson, PATENTS

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(Some of the patents were in process when Infineon acquired Ericsson Microelectronics in 2002 and were probably withdrawn during the process, as indicated for a few patents below.)

1. T. Johansson, J.-M. Gobbi, "Equipment for minimising parasitic capacitance arising between substrate and coupling capacitor uses capacitive coupling between different function blocks in an integrated circuit", Swedish Patent Nr. 470 115, filed 1992-05-02.
2. T. Johansson, J.-M. Gobbi, "High capacitance capacitor for an IC and method of mfr. is formed in a substrate beneath existing metallisation so that no additional substrate surface is required", Swedish Patent Nr. 470 415, filed 1992-07-02.
3. T. Johansson, I. Hamberg, L. Leighton, "Layout for radio frequency power transistors", US Patent 5,488,252, filed 1994-08-16.
4. T. Johansson, L. Leighton, "Ballast monitoring for radio frequency power transistors", US Patent 5,907,180, filed 1994-11-03.
5. T. Johansson, L. Leighton, "Emitter ballast bypass for radio frequency power transistors", US Patent 5,684,326, filed 1995-02-24.
6. T. Johansson, J.-M. Gobbi, "Low loss inductors with active compensation for very high Q filters", Swedish Patent 506,626, filed 1995-11-27.
7. T. Johansson, H. Norström, "Inductors for Integrated Circuits", Swedish Patent 510,443, filed 1996-05-31.
8. L. Leighton, T. Johansson, B. Skoglund, "Improved thermal ballasting in a RF transistor", US Patent 5,804,867, filed 1996-10-02.
9. T. Johansson, L. Leighton, "Field Shield Isolation", Swedish Patent 509,780, filed 1997-07-04.
10. T. Johansson, T. Arnborg, "Linearity Improvement", Swedish Patent No. 511,891, filed 1997-08-29.
11. A. Litwin, T. Johansson, "Method and device for interconnect radio frequency power SiC field effect transistors", Swedish Patent 520,119, filed 1998-10-13.
12. T. Johansson, J.-C. Nyström, "Method for semiconductor manufacturing", Swedish Patent 514,792, filed 1998-11-04.
13. T. Johansson, J.-C. Nyström, "Semiconductor device with deep substrate contacts", Swedish Patent 515,158, filed 1999-02-10.

14. T. Johansson, "Improved RF-power transistor", Swedish Patent 515,836, filed 1999-05-17.
15. T. Johansson, "Collector-up RF power transistor", Swedish Patent 516,338, filed 1999-05-31.
16. T. Johansson, "Method to fabricate a MOS Transistor", Swedish Patent 517,452, filed 1999-09-01.
17. H. Norström, C. Björmander, T. Johansson, "Self-aligned trench", Swedish Patent 518,533, filed 1999-09-17.
18. H. Norström, T. Johansson, "Indium-enhanced bipolar transistors", Swedish Patent 517,434, filed 1999-10-08.
19. H. Norström, T. Johansson, "Self-aligned base etch for bipolar transistors", Swedish Patent 517,833, filed 1999-11-26.
20. T. Arnborg, T. Johansson, "Bipolar transistor structure", Swedish Patent 517,711, filed 1999-12-02.
21. H. Norström, T. Arnborg, T. Johansson, "Implanted launcher layer", Swedish Patent 518,710, filed 2000-06-26.
22. C. Björmander, H. Norström, T. Johansson, "Cavity Inductor", Swedish Patent 519,893, filed 2000-11-09.
23. T. Johansson, H. Norström, M. Carlsson, "Trench-Plug-Shield Inductor", Swedish Patent 520,093, filed 2000-12-13.
24. H. Norström, T. Johansson, P. Algotsson, "Semiconductor Processing", Swedish Patent 522,527, filed 2001-05-04.
25. T. Arnborg, T. Johansson, "Low-noise MOS device", Swedish Patent 522,714, filed 2001-07-13.
26. H. Norström, T. Johansson, "Silicon-Germanium Mesa Transistor", Swedish Patent 522,891, filed 2001-11-09.
27. H. Norström, T. Johansson, "Lateral PNP", Swedish Patent 522,890, filed 2001-11-15.
28. T. Johansson, "PMOS Varactor", Swedish Patent 520,590, filed 2001-11-15.
29. H. Norström, T. Johansson, S. Sahl, "Varactor with linear frequency response", filed to Swedish Patent Office, Jan 18, 2002. Withdrawn.
30. H. Norström, T. Johansson, A. Lindgren, "Nitride seal", filed to Swedish Patent Office, Jan 21, 2002. Withdrawn.

31. H. Norström, T. Johansson, "Lateral PNP with polysilicon emitter", filed to Swedish Patent Office, Feb 13, 2002. Withdrawn?
32. H. Norström, T. Johansson, "Selective Si", Swedish Patent 529,916, filed 2022-05-08.
33. H. Norström, T. Johansson, "LDMOS shield", filed to Swedish Patent Office, Oct 24, 2003. Withdrawn?
34. H. Norström, T. Johansson, "Metal resistor for LDMOS-IC", filed to Swedish Patent Office, Oct 24, 2003. Withdrawn?
35. T. Johansson, "Capacitor for SOI", Swedish Patent 527,487, filed 2004-02-03.
36. T. Johansson, H. Norström, "Vertical bipolar transistor on SOI", US Patent 7618865, filed 2004-08-31.
37. T. Johansson, H. Norström, P. Algotsson, K. Andersson, "Layout for buried collector", filed to European Patent Office, Sep 2004. Withdrawn?
38. T. Johansson, H. Norström, "Integrated Injection logic in a double-poly bipolar process", US Patent 7456069, filed 2004-10-06.
39. T. Arnborg, T. Johansson, "Ultra-high tuning range capacitors", filed to European Patent Office, filed 2004-12-13. Withdrawn.
40. U. Gustavsson, E. G. Larsson, T. Johansson, "Reference Distribution for Large Antenna Arrays", EP3593459B1, filed 2017-03. US 11,463,625, approved 2022-10-04.